

Penelope Prospect

1801 Boulevard Street
Fremont, California 94538
(510) 555-5555 email@domain.com

OBJECTIVE: To obtain a position as a Senior Mechanical Engineer

EDUCATION **UNIVERSITY OF CALIFORNIA BERKELEY** Berkeley, CA
Etcheverry School of Mechanical Engineering
Bachelor of Science in Engineering May 1995
Major in Mechanical Engineering
Cumulative GPA: 3.8

EXPERIENCE **ELECTROTECH CONSULTING** San Jose, CA
Hardware Design Engineer June 2000 - Present

- Semiconductor solutions, designed board-level logic and PCB for next generation product.
- Also responsible for bringing up system, for diagnostics, and for interfacing with manufacturer/vendor
- High speed digital design (>100MHZ). Working knowledge in C/C++, Verilog, FCC issues, analog or RF design

ADAPTEC Milipitas, California
Attractions Host - College Programs July 1995-May2000

- Member of the ASIC Engineering team
- Design for test (DFT) for the company's next generation high speed storage products
- Developed test methodologies for product validation and defects detection
- Test Pattern generation for device characterization. Simulation to test vector conversion and verification. Product and characterization test program development and characterization
- Verilog and analog, IC test methodology and simulation to test vector conversion and verification

ACTIVITIES **American Engineering Institute**
• Silver Class Member, Two patents, membership for 6 years

Women's Engineering Association
• Contributing editor for bi-annual publication

REFERENCES Professional and personal references available upon request.